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PTO/SB/21 (01-03)
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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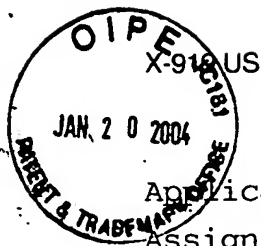
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| TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i> | Application / Conf. No. | 10/001,871 / 3401 |
| | Filing Date | November 19, 2001 |
| | First Named Inventor | Stephen M. Douglass |
| | Examiner Name | Unassigned |
| | Art Unit | 2818 |
| | Patent No. | Technology Center 2100 |
| Mail Stop: | | |
| Express Mail Receipt No. | | |
| Total Number of Pages in This Submission | | Attorney Docket Number X-919 US |

| ENCLOSURES (check all that apply) | | |
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| <input type="checkbox"/> Preliminary Amendment | <input type="checkbox"/> Drawing(s) | <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) |
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| <input type="checkbox"/> Certified Copy of Priority Document(s) | | |
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| Remarks | | |

| SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT | | |
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| Firm or Customer Number | 24309 (Customer Number) | Reg. Number 51,959 |
| Attn: Justin Liu | | |
| Signature | | |
| Date | January 16, 2004 | Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040 |

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This collection of information is required by 37 CFR 1.17 and 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Stephen M. Douglass et al.
Assignee: Xilinx, Inc.
Title: "Method and Apparatus for Processing Data Within a Programmable Gate Array Using Fixed and Programmable Processors"

Serial No.: 10/001,871 File Date: November 19, 2001
Examiner: Unknown Art Unit: 2818
Docket No.: X-919 US Conf. No.: 3401

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the One Hundred Two (102) references listed in the attached Form PTO-1449. A copy of each is enclosed herein.

These references were cited in related U.S. patent applications. This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu
Attorney for Applicants
Reg. No. 51,959

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Julie Matthews
Name

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PTO/SB/08A (10-01)

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Substitute for Prop. 1449A/PTO

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| First Named Inventor | Stephen M. Douglass |
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| Art Unit | 2818 |
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| Examiner Name | Unknown |
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Sheet 2 of 7

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| Application / C nf. N . | 10/001,871 / 3401 |
| Filing Date | November 19, 1999 |
| First Named Inventor | Stephen M. Douglass |
| Art Unit | 2818 |
| Examiner Name | Unknown |
| Attorney Docket Number | X-919 US |

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OTHER - NON PATENT LITERATURE DOCUMENTS

| Examiner Initials * | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
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| | | Applicant / Cnf. No. | 10/001,871 / 3401 | | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | Filing Date | November 19, 2001 | | |
| | | First Named Inventor | Stephen M. Douglass | | |
| | | Art Unit | 2818 | | |
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| OTHER – NON PATENT LITERATURE DOCUMENTS | | | |
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| Examiner Initials * | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124. | |
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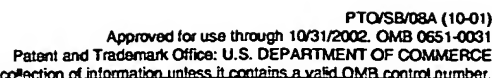
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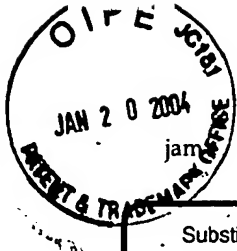
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Sheet 7 of 7

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| First Named Inventor | Stephen M. Douglass |
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| Examiner Name | Unknown |
| Attorney Docket Number | X-919 US |

U.S. PATENT DOCUMENTS

| Examiner Initials * | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|------------------------|--------------------------|-------------------------------|--------------------------------|--|---|
| | | Number - Kind Code (if known) | | | |
| | | US- 6,519,753 | 02-11-03 | Ang | |
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